

Abstract of the Disclosure:

A phase locked loop has a voltage controlled oscillator with two tuning inputs. One tuning input can be supplied with a feedback signal via a frequency divider in a conventional  
5 phase locked loop. A frequency word, which is provided anyway for the purpose of setting the division ratio of the PLL and hence for the purpose of frequency preselection, is used not only to supply the frequency divider but also for compensatory tuning of frequency-determining components in the oscillator.  
10 The phase locked loop allows, particularly in inexpensive open loop modulation methods, a significant reduction in the frequency drift by virtue of a smaller or disappearing discrepancy in the tuning voltage in conjunction with a reduction in the memory effect of capacitors in loop filters  
15 using particularly simple circuitry measures.

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